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Docket No.: SON-2788
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Letters Patent of:

Confirmation No.: 8767

Katsutoshi Moriyama et al.

Patent No.: 7,130,224

Issued: October 31, 2006

For: COMPOUND STORAGE CIRCUIT AND
SEMICONDUCTOR DEVICE INCLUDING
COMPOUND STORAGE CIRCUIT

REQUEST FOR CERTIFICATE OF CORRECTION

Attention: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

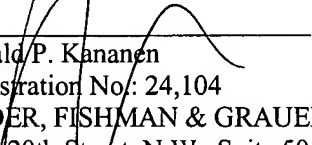
Dear Sir:

It is respectfully requested that a Certificate of Correction be issued in connection with the above-identified patent. It appears that a mistake was recorded through the fault of the Patent and Trademark Office in the printing of the patent as clearly disclosed by the records of the Office within the meaning of 35 USC § 254. Accordingly, two copies of the special Certificate of Correction are attached hereto. It is believed that the error noted is an error of consequence involving the title of the invention and thus warrants the granting of a Certificate of Correction. Copies of the first page of the application showing the correct title, the declaration showing the correct title, and the Official Filing Receipt with the correct title are enclosed for the convenience of the PTO. It is believed that the error was made on the part of the PTO and that no government fee is required.

Should any costs be incurred, please consider this authorization to charge Deposit Account No. 18-0013.

Dated: June 11, 2007

Respectfully submitted,

By 
Ronald P. Kananen
Registration No.: 24,104
RADER, FISHMAN & GRAUER PLLC
1233 20th Street, N.W., Suite 501
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Attorney for Applicant

Certificate
JUN 13 2007
of Correction

JUN 13 2007

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,130,224

DATED : October 31, 2006

INVENTOR(S): Katsutoshi MORIYAMA et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title Page:

Item (54) should be read as follows:

-- COMPOUND STORAGE CIRCUIT AND SEMICONDUCTOR DEVICE INCLUDING
COMPOUND STORAGE CIRCUIT --.

MAILING ADDRESS OF SENDER:

Customer No. 23353
Rader, Fishman & Grauer PLLC
1233 20th Street, NW
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Washington, DC 20036

PATENT NO. 7,130,224

Our Ref.: SON-2788
FORM PTO 1050 (REV. 3-82)

NOV 13 2007



- 1 -

S03P0810

DESCRIPTION

COMPOUND STORAGE CIRCUIT AND SEMICONDUCTOR DEVICE INCLUDING COMPOUND STORAGE CIRCUIT

Technical Field

The present invention relates to a compound storage circuit including a volatile storage circuit and a nonvolatile storage circuit that are connected in parallel to each other, and to a semiconductor device including the compound storage circuit.

Background Art

In a semiconductor device that is incorporated in an electronic computer or the like, such as a personal computer, and that performs predetermined processing, a volatile storage circuit has been provided according to need, so that the processing is performed while information necessary for the processing is sequentially stored into the volatile storage circuit.

Such a volatile storage circuit stores information by supplying electric power and has a characteristic of high writing and reading speed. In contrast, the volatile storage circuit has a characteristic of losing stored information when power supply is interrupted due to a power-

13 2007

Attorney's Docket No. SON-2788

DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION
English Language Declaration

As below named inventors, we hereby declare that:

Our residence, post office address and citizenship are as stated below next to our names.

We believe we are the original, first and joint inventors of the subject matter which is claimed and for which a patent is sought on the invention entitled

COMPOUND STORAGE CIRCUIT AND SEMICONDUCTOR DEVICE INCLUDING COMPOUND STORAGE CIRCUIT
the specification of which

(check one)

is attached hereto.

X was filed on July 22, 2003 as

Application Serial No. PCT/JP03/09295
and was amended on _____

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, §1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, §119 of any foreign application(s) for patent of inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)			Priority Claimed
P2002-220423	Japan	29/07/2002	X
(Number)	(Country)	(Day/Month/Year Filed)	Yes No
PCT/JP03/09295	Japan	22/07/2003	X
(Number)	(Country)	(Day/Month/Year Filed)	Yes No
(Number)	(Country)	(Day/Month/Year Filed)	Yes No

We hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s) listed below and insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code §112, I acknowledge the duty to disclose material to patentability as defined in Title 37, Code of Federal Regulations, §1.56 and 1.63(d) which became available between the filing date of the prior application and the national or PCT international filing date of this application:

(Application Serial No.)	(Filing Date)	(Status)
		(patented, pending, abandoned)

We hereby declare that all statements made herein of our own knowledge are true and that all statements made on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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Attorney's Docket Number: SON-2788

English Language Declaration

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith.

Ronald P. Kananen, Reg. No. 24,104; Ralph T. Rader, Reg. No. 28,772;
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Inventor's signature	<i>Katsutoshi Moriyama</i>	Date January 4, 2005
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	2-3-2 MOMOCHIHAMA, SAWARA-KU, FUKUOKA-SHI, FUKUOKA, JAPAN	
Full name of second joint inventor	Hironobu MORI	
Second Inventor's signature	<i>Hironobu Mori</i>	Date January 4, 2005
Residence	Nagasaki, Japan	
Citizenship	JAPANESE	
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	2-3-2 MOMOCHIHAMA, SAWARA-KU, FUKUOKA-SHI, FUKUOKA, JAPAN	
Full name of third joint inventor	Nobumichi OKAZAKI	
Third Inventor's signature	<i>Nobumichi Okazaki</i>	Date Jan. 11, 2005
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(Supply similar information and signature for subsequent joint inventors.)

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UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
 United States Patent and Trademark Office
 Address: COMMISSIONER FOR PATENTS
 P.O. Box 1450
 Alexandria, Virginia 22313-1450
 www.uspto.gov

APPL NO.	FILING OR 371(C) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	TOT CLMS	IND CLMS
10/522,316	01/25/2005	2827	2110	SON-2788	18	2

CONFIRMATION NO. 8767

CORRECTED FILING RECEIPT



OC000000023636731

23353
 RADER FISHMAN & GRAUER PLLC
 LION BUILDING
 1233 20TH STREET N.W., SUITE 501
 WASHINGTON, DC 20036

Date Mailed: 05/02/2007

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please mail to the Commissioner for Patents P.O. Box 1450 Alexandria Va 22313-1450. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Katsutoshi Moriyama, Saga, JAPAN;
 Hironobu Mori, Nagasaki, JAPAN;
 Nobumichi Okazaki, Kanagawa, JAPAN;

Assignment For Published Patent Application

Sony Corporation, Tokyo, JAPAN

Power of Attorney: The patent practitioners associated with Customer Number 23353.

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP03/09295 07/22/2003

Foreign Applications

JAPAN 2002-220423 07/29/2002

If Required, Foreign Filing License Granted: 05/01/2007

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is US10/522,316

DVLE

Projected Publication Date: Not Applicable

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Non-Publication Request: No

Early Publication Request: No

Title

Compound Storage Circuit and Semiconductor Device including Compound Storage Circuit

Preliminary Class

365

PROTECTING YOUR INVENTION OUTSIDE THE UNITED STATES

Since the rights granted by a U.S. patent extend only throughout the territory of the United States and have no effect in a foreign country, an inventor who wishes patent protection in another country must apply for a patent in a specific country or in regional patent offices. Applicants may wish to consider the filing of an international application under the Patent Cooperation Treaty (PCT). An international (PCT) application generally has the same effect as a regular national patent application in each PCT-member country. The PCT process **simplifies** the filing of patent applications on the same invention in member countries, but **does not result** in a grant of "an international patent" and does not eliminate the need of applicants to file additional documents and fees in countries where patent protection is desired.

Almost every country has its own patent law, and a person desiring a patent in a particular country must make an application for patent in that country in accordance with its particular laws. Since the laws of many countries differ in various respects from the patent law of the United States, applicants are advised to seek guidance from specific foreign countries to ensure that patent rights are not lost prematurely.

Applicants also are advised that in the case of inventions made in the United States, the Director of the USPTO must issue a license before applicants can apply for a patent in a foreign country. The filing of a U.S. patent application serves as a request for a foreign filing license. The application's filing receipt contains further information and guidance as to the status of applicant's license for foreign filing.

Applicants may wish to consult the USPTO booklet, "General Information Concerning Patents" (specifically, the section entitled "Treaties and Foreign Patents") for more information on timeframes and deadlines for filing foreign patent applications. The guide is available either by contacting the USPTO Contact Center at 800-786-9199, or it can be viewed on the USPTO website at <http://www.uspto.gov/web/offices/pac/doc/general/index.html>.

For information on preventing theft of your intellectual property (patents, trademarks and copyrights), you may wish to consult the U.S. Government website, <http://www.stopfakes.gov>. Part of a Department of Commerce initiative, this website includes self-help "toolkits" giving innovators guidance on how to protect intellectual property in specific countries such as China, Korea and Mexico. For questions regarding patent enforcement issues, applicants may call the U.S. Government hotline at 1-866-999-HALT (1-866-999-4158).

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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15

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